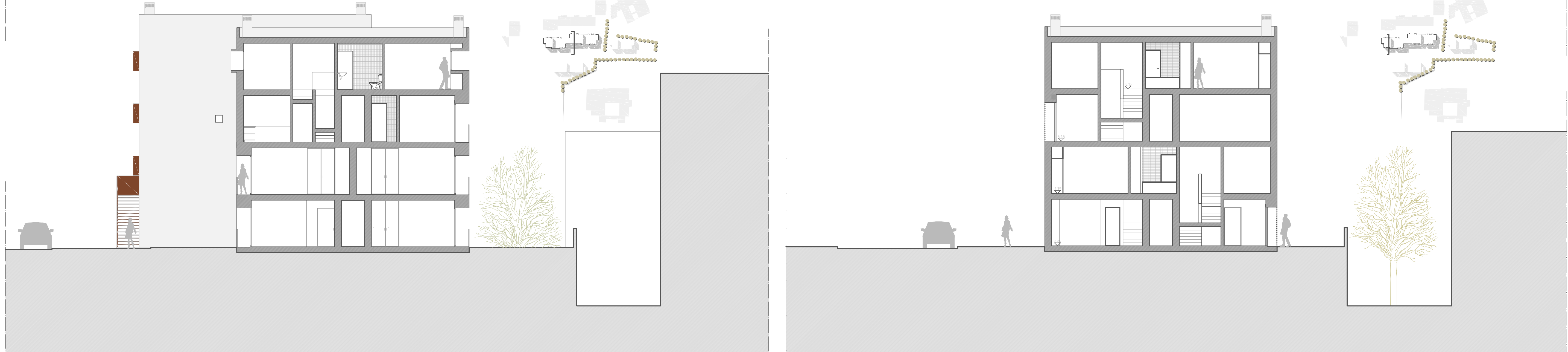


Corte A_A'



Corte B_B'



Corte D_D'

Corte C_C'

